

IN THE CLAIMS

Please amend the claims as follows:

1. (Original) A method for downloading software to a device, comprising:
connecting a source of software to a universal serial bus (USB) port communicating with the device;
downloading the software from the source to the device; and
resetting the device by undertaking at least one of: resetting registers in a processor of the device except for USB-associated registers; maintaining a USB transceiver associated with the device in an "on" state; resetting values in a RAM associated with the device except for USB-associated values; and setting a flash memory associated with the device in a data mode.
2. (Original) The method of Claim 1, wherein prior to said resetting act, the USB-associated registers of the processor are configured with non-default values, and the method further comprises:
resetting all registers in the processor except for the USB-associated registers; and
maintaining the non-default values in the USB-associated registers during the resetting act.
3. (Original) The method of Claim 1, further comprising maintaining the USB transceiver in the "on" state during the resetting act, wherein the USB transceiver communicates with the source of software through the USB port.
4. (Original) The method of Claim 1, wherein prior to said resetting act, the USB-associated values in the RAM have non-default values, and the method further comprises:
resetting RAM values to default values except for the USB-associated values; and
maintaining the USB-associated values during the resetting act.
5. (Original) The method of Claim 1, wherein the resetting act comprises all of:
resetting all registers in a processor of the computer except for USB-associated registers;
maintaining a USB transceiver associated with the computer in an "on" state; resetting values in a

RAM associated with the computer except for any USB-associated values; and setting a flash memory associated with the computer in a data mode.

6. (Original) The method of Claim 1, wherein the device is a CDMA wireless device.

7. (Original) A wireless communication device, comprising:
at least one processor having registers, at least some of which are USB-associated registers containing pre-reset values;

at least one USB transceiver configured for communicating with the USB-associated registers of the processor and with a source of software external to the wireless communication device; and

logic executable by the processor for resetting the wireless communication device under at least one reset condition by:

resetting registers other than the USB-associated registers to their default value,

maintaining the pre-reset values in the USB-associated registers at least while the non-USB-associated registers are reset, and

maintaining the USB transceiver energized during the act of resetting the wireless communication device.

8. (Original) The wireless communication device of Claim 7, wherein the reset condition is related to downloading of software from the source of software.

9. (Original) The wireless communication device of Claim 7, further comprising at least one RAM communicating with the processor, the logic causing the processor during the resetting act to reset some RAM values to default values while maintaining USB-related non-default values.

10. (Original) The wireless communication device of Claim 7, further comprising flash memory communicating with the processor, the logic causing the processor to configure the flash memory in a data mode during the act of resetting.

11. (Original) The wireless communication device of Claim 7, wherein the wireless communication device is a CDMA device.

Claims 12-16. (Cancelled)

17. (Currently Amended) An apparatus for downloading software to a device, comprising:

means for connecting a source of software to a universal serial bus (USB) port communicating with the device;

means for downloading the software from the source to the device; and

means for resetting the device by undertaking at least one of: means for resetting registers in a processor of the device except for USB-associated registers; means for maintaining a USB transceiver associated with the device in an "on" state; means for resetting values in a RAM associated with the device except for USB-associated values; and means for setting a flash memory associated with the device in a data mode.

18. (New) An apparatus of Claim 17, wherein prior to said means for resetting, the USB-associated registers of the processor are configured with non-default values, and the apparatus further comprises:

means for resetting all registers in the processor except for the USB-associated registers; and

means for maintaining the non-default values in the USB-associated registers during the means for resetting act.

19. (New) An apparatus of Claim 17, further comprising means for maintaining the USB transceiver in the "on" state during the means for resetting, wherein the USB transceiver communicates with the source of software through the USB port.

20. (New) An apparatus of Claim 17, wherein prior to said means for resetting, the USB-associated values in the RAM have non-default values, and the apparatus further comprises:

means for resetting RAM values to default values except for the USB-associated values;

and

means for maintaining the USB-associated values during the means for resetting.

21. (New) An apparatus of Claim 17, wherein the means for resetting comprises: means for resetting all registers in a processor of the computer except for USB-associated registers; means for maintaining a USB transceiver associated with the computer in an "on" state; means for resetting values in a RAM associated with the computer except for any USB-associated values; and means for setting a flash memory associated with the computer in a data mode.